

CMD065N04/CMU065N04

N-Channel Enhancement Mode Field Effect Transistor

General Description

The 065N04 uses advanced SGT technology to provide excellent $R_{DS(ON)}$. This device is suitable for use as a Battery protection or in other Switching application.

Product Summary

BVDSS	RDS(on)	ID
40V	5.8mΩ	80A

Applications

- Load Switch
- Networking DC-DC Power System
- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA

Features

- Fast switching
- Lower On-resistance
- 100% avalanche tested
- RoHS Compliant

TO-252/251 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current	80	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current	56	A
I_{DM}	Pulsed Drain Current	320	A
EAS	Single Pulse Avalanche Energy ¹	100	mJ
$P_D @ T_c = 25^\circ C$	Total Power Dissipation	52	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient ²	---	25	°C/W
$R_{\theta JC}$	Thermal Resistance Junction -Case	---	2.4	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	40	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=28\text{A}$	---	5	5.8	$\text{m}\Omega$
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=4.5\text{V}$, $I_D=25\text{A}$	---	7.6	8.8	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D =250\mu\text{A}$	1	---	3	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=32\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_D=20\text{A}$	---	9	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	2	---	Ω
Q_g	Total Gate Charge	$V_{\text{DD}}=20\text{V}$, $I_D=12\text{A}$ $V_{\text{GS}}=4.5\text{V}$	---	10	---	nC
Q_{gs}	Gate-Source Charge		---	3.5	---	
Q_{gd}	Gate-Drain Charge		---	4	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=15\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=3.3\Omega$	---	5	---	ns
T_r	Rise Time		---	9	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	25	---	
T_f	Fall Time		---	15	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	1000	---	pF
C_{oss}	Output Capacitance		---	270	---	
C_{rss}	Reverse Transfer Capacitance		---	16	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Diode continuous forward current	$V_G=V_D=0\text{V}$, Force Current	---	---	80	A
$I_{s,\text{pulse}}$	Diode pulse current		---	---	320	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=28\text{A}$, $T_J=25^\circ\text{C}$	---	0.89	1.2	V

Notes:

- 1.The EAS data shows Max. rating .The test condition is $V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.5\text{mH}$, $I_{\text{AS}}=20\text{A}$.
2. The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.

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Typical Characteristics

